## **REMARKS**

Applicants have amended claims 2 and 5 to overcome the objection to the claims. Applicants have also amended claims 2-4 and 6 to improve English usage.

Applicants thank the Examiner for allowing 5-10 and indicating allowable subject matter.

Claims 2 and 5 have been objected to because of informalities. Applicants have amended the claims to correct the informalities according to the Examiner's suggestion.

Claim 1 have been rejected under 35 USC 102(b) as anticipated by U.S. Patent No. 5,952,948 (Proebsting). Applicants respectfully traverse this rejection.

Original claim 1 states that each of the pixels comprises a serial-to-parallel converter and a DA converter. This means that each pixel includes two converters. The Examiner contends that Proebsting's shift register 102 corresponds to the claimed serial-to-parallel converter and Proebsting's DAC 106 to the claimed DA converter. Applicants point out that Proebsting's display device has only one shift register 102 that is placed outside Proebsting's LCD panel 108. Likewise, Proebsting's DA converters 106 are placed outside the LCD panel 108. See, for example, FIG. 1 of Proebsting. On the other hand, in the claimed display device each of the pixels includes a serial-to-parallel converter and a DC converter. No such structure is disclosed in Proebsting.

The rejection of claim 1 under 35 USC 102(b) on Proebsting should be withdrawn because Proebsting does not teach or suggest the claimed serial-to-parallel converters and DA converters that are proved in the pixels.

In the event that the transmittal is separated from this document and the Patent and Trademark Office determines that an extension and/or other relief is required, applicants petition for any required relief including extensions of time and authorize the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952, referencing Docket No. 492322013500.

By:

Respectfully submitted,

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